

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph bridging pages 3 and 4 as follows:

According to this invention, a method for calculating pin-to-pin delay time $T_{\text{iopath_aged}}$ and block-to-block delay time $T_{\text{connect_aged}}$ is provided. The pin-to-pin delay time $T_{\text{iopath_aged}}$ is delay time of a signal passing between an input pin and an output pin of a logic block. The block-to-block delay time $T_{\text{connect_aged}}$ is delay time of a signal passing between said two logic blocks connected to each other ~~in a computer~~. Furthermore, this method comprises the following steps:

Please amend the first full paragraph on page 6 as follows:

According to this invention, a computer software product for calculating pin-to-pin delay time $T_{\text{iopath_aged}}$ and block-to-block delay time $T_{\text{iopath_aged}}$ is provided. The pin-to-pin delay time $T_{\text{iopath_aged}}$ is delay time of a signal passing between an input pin and an output pin of a logic block. The block-to-block delay time $T_{\text{connect_aged}}$, which is delay time of a signal passing between said two logic blocks connected to each other ~~in a computer~~. And then, the computer software product makes a computer execute the following processes: